

# Multi-Purpose Single Transformers

Offered in a Wide Range of Inductances and Turns Ratios, suited for a variety of requirements including LAN Networking and other Data Communications applications.

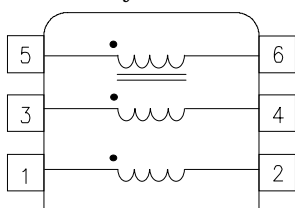
High Potential Test 1000 Vrms min.

Variations in electrical parameters and lead lengths are available. For alternate schematic types refer to page 13.

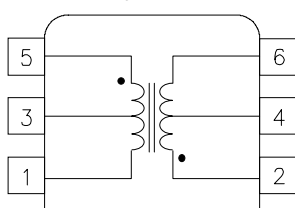
Electrical Specifications at 25°C

Part Number	Turns Ratio ( $\pm 5\%$ )	OCL min. ( $\mu\text{H}$ )	Min. Pri. ET-Const. ( $\text{V}\cdot\mu\text{sec}$ )	Rise Time max. (ns)	Pri-Sec $C_{\text{www}}$ max. (pF)	Leakage Inductance max. ( $\mu\text{H}$ )	Pri DCR max. ( $\Omega$ )	Schem. Style
T-50100	1:1:1	5000	25.0	10.5	60	1.3	3.9	A
T-50101	1:1:1	2000	16.0	8.2	37	0.8	2.5	A
T-50102	1:1:1	500	8.5	5.3	32	0.4	1.3	A
T-50103	1:1:1	200	5.0	4.2	18	0.3	0.9	A
T-50104	1:1:1	50	5.2	5.6	18	0.5	1.3	A
T-50105	1:1:1	20	3.2	4.1	21	0.4	0.9	A
T-50106	1:1:1	10	3.7	5.1	22	0.5	0.9	A
T-50109	1CT:1CT	5000	25.0	10.5	60	1.3	3.9	G
T-50110	1CT:1CT	2000	16.0	8.2	37	0.8	2.5	G
T-50111	1CT:1CT	500	8.5	5.3	32	0.4	1.3	G
T-50112	1CT:1CT	200	5.0	4.2	18	0.3	0.9	G
T-50113	1CT:1CT	50	5.2	5.6	18	0.5	1.3	G
T-50114	1CT:1CT	20	3.2	4.1	21	0.4	0.9	G
T-50115	1CT:1CT	10	3.7	5.1	22	0.5	0.9	G
T-50117	2CT:1CT	5000	25.0	10.5	60	1.3	3.9	G
T-50118	2CT:1CT	2000	16.0	11.0	19	1.8	2.5	G
T-50119	2CT:1CT	500	8.5	5.6	12	1.2	1.4	G
T-50120	2CT:1CT	200	5.0	3.2	8	0.8	0.9	G
T-50121	2CT:1CT	50	5.2	5.2	12	1.3	1.3	G
T-50122	2CT:1CT	20	5.8	7.5	12	1.5	1.3	G
T-50123	2CT:1CT	10	3.7	4.5	9	1.5	0.9	G

Schematic Style "A"



Schematic Style "G"



## Dual & Triple Multi-Purpose Transformers

Fast Rise Times - High Isolation 2000 Vrms

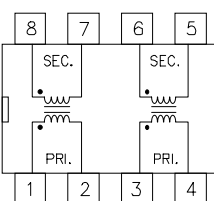
Economical Density: 2 or 3 Per Package

Transfer Molded DIP / SMD Packages

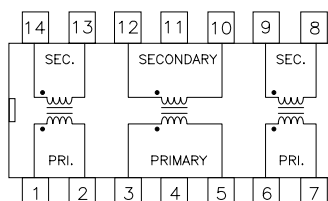
Electrical Specifications at 25°C

Dual 8 Pin Part No.	Triple 14 Pin Part No.	Turns Ratio ( $\pm 5\%$ )	OCL ( $\pm 20\%$ ) ( $\mu\text{H}$ )	E-T min. $\text{V}\cdot\mu\text{S}$	Rise Time max. (ns)	Pri./Sec. $C_{\text{www}}$ max. (pF)	$L_L$ max. ( $\mu\text{H}$ )	DCR max. ( $\Omega$ )
T-11400	T-10700	1:1	30	1.8	3.0	8	0.20	0.20
T-11401	T-10701	1:1	35	1.8	3.0	8	0.20	0.20
T-11402	T-10702	1:1	50	2.1	3.0	9	0.20	0.20
T-11403	T-10703	1:1	75	2.3	3.0	10	0.20	0.25
T-11404	T-10704	1:1	80	2.3	3.0	10	0.20	0.25
T-11405	T-10705	1:1	100	2.7	3.5	10	0.20	0.25
T-11406	T-10706	1:1	150	3.0	3.5	12	0.25	0.30
T-11407	T-10707	1:1	200	3.5	3.5	15	0.25	0.40
T-11408	T-10708	1:1	250	3.5	3.5	15	0.25	0.40

Dual Schematic



Triple Schematic



Low Profile DIP / SMD  
8 & 14 Pin Packages  
See pg. 40,  
figs. 4, 5 & 6

