

5.0ns Delay 50 Ohm Impedance Single-in-Line Delay Module

OPERATING SPECIFICATIONS

Pulse Overshoot (Pos) 5% to 10%, typical
 Pulse Distortion (S) 3% typical
 Attenuation 0.25 dB maximum
 Working Voltage 25 VDC maximum
 Dielectric Strength 100VDC minimum
 Insulation Resistance 1,000 Megohms min. @ 100VDC
 Temperature Coefficient 70 ppm/°C, typical
 Band Width (f_c)35/tr approx.
 Operating Temperature Range -55° to +125°C
 Storage Temperature Range -65° to +150°C

ELECTRICAL SPECIFICATIONS @ 25°C

Measured @ 50% Level on the Leading Edge
 Total Delay 5.0 ns ± 0.25 ns
 Rise Time (20%-80%) 1.8 ns max.
 Characteristic Impedance 50.0 Ohms ± 10%
 D.C. Resistance 0.8 Ohms max.
 Attenuation 0.5 dB max.

TEST CONDITIONS

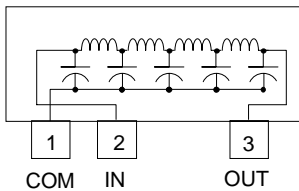
(Measurements made at 25°C)

Input Rise Time 2.0 ns max.
 Input Pulse Period 1000 ns
 Input Pulse Width 2000 ns

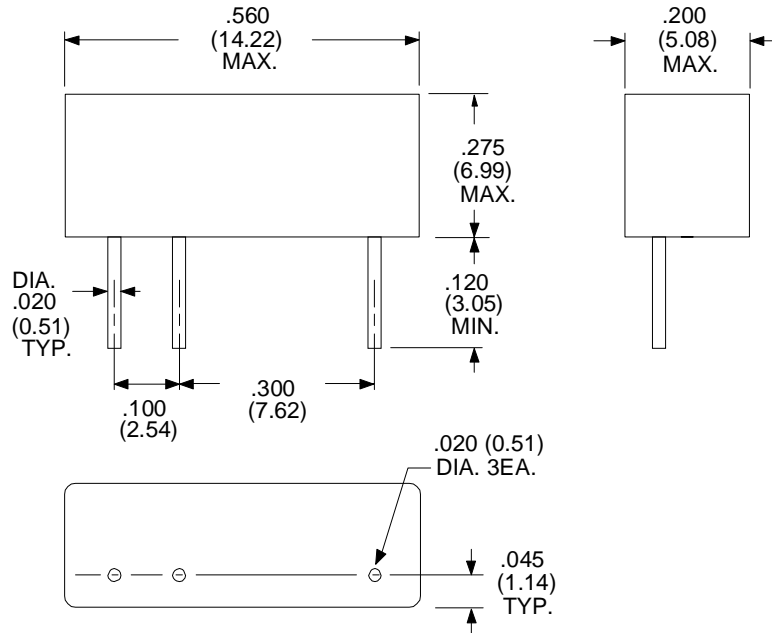
ENVIRONMENTAL

All units are designed to meet the applicable portions of MIL-D-23859, MIL-D-83531 and are capable of meeting the environmental requirements of MIL-STD-202 for moisture resistance, vibration shock, humidity and life.

SCHMATIC DIAGRAM



PHYSICAL DIMENSIONS
inches (mm)



RHOMBUS P/N: **SL2C-5-50**

CUST P/N:

NAME:

DATE: **02/15/2000**

SHEET: 1 OF 1