

# T-1316G

## HDSL2

### TRANSFORMER

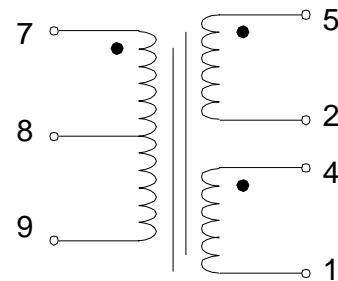
Designed for HDSL2 applications

Compatible with Level One  
SK70740/41/42

Low Profile "Touch Tone" design

PARAMETER		UNITS
Turns Ratio <sup>1</sup>	1 : 2 ±3%	
Inductance <sup>2</sup>	1.75 ±8%	mH
Longitudinal Balance 20 kHz to 600 kHz	50 min.	dB
Primary DCR (Pins 7-9)	0.6 Max.	Ω
Secondary DCR (Pins 5-2 or 4-1)	0.6 Max.	Ω
Frequency Response 40kHz - 300kHz	±0.2	dB
Insertion Loss @ 100kHz	0.3	dB
Isolation (HI-POT)	3000	V <sub>RMS</sub>
Primary DC	160	mA
Return Loss @ 100kHz	16.5 min.	dB
THD (10 KHz, 4.95V <sub>RMS</sub> )	-75 Min.	dB

#### SCHEMATIC DIAGRAM



Line Side Pins 7 - 9  
Chip Side Pins 5 - 2, 4 - 1

Primary is Line Side  
Secondary is Chip Side

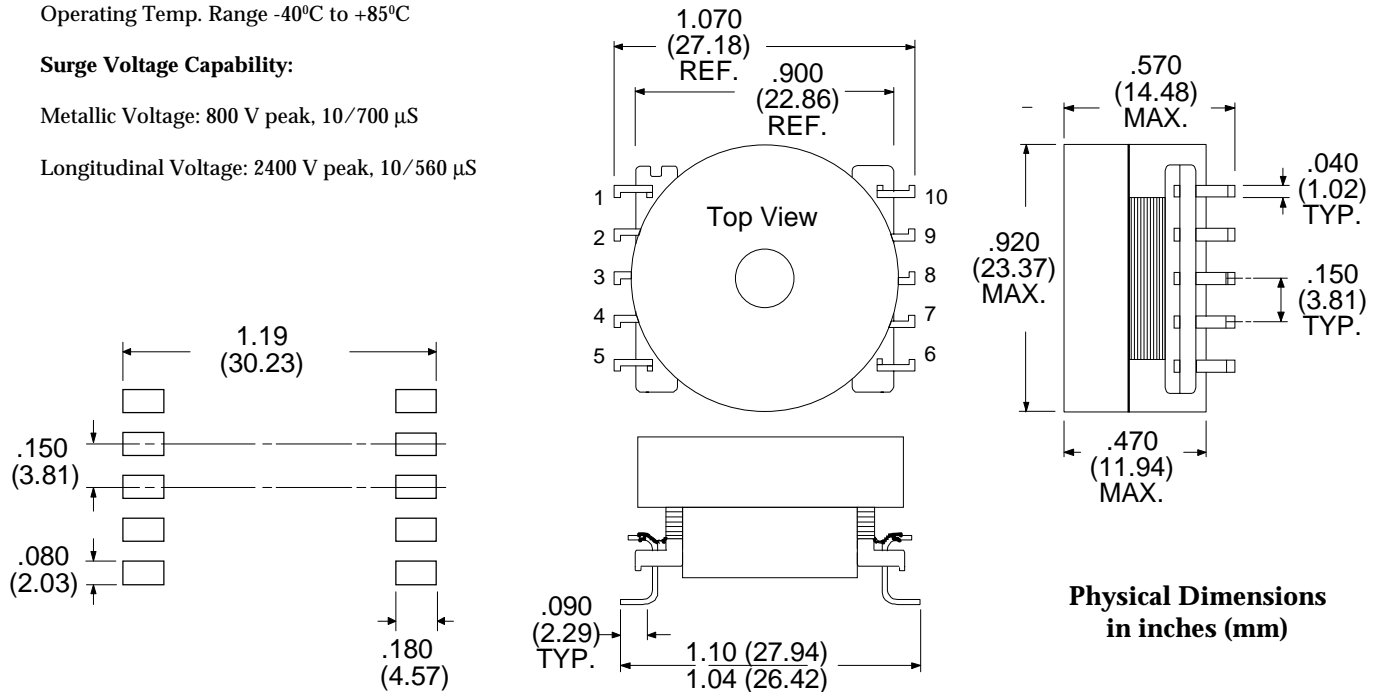
- Short pins 2 & 4
- Line Side, Pins (7 - 9) @ 10KHz and 100 mV

Operating Temp. Range -40°C to +85°C

#### Surge Voltage Capability:

Metallic Voltage: 800 V peak, 10/700 μS

Longitudinal Voltage: 2400 V peak, 10/560 μS



Physical Dimensions  
in inches (mm)

Notch on bobbin indicates pin #1

4/22/99