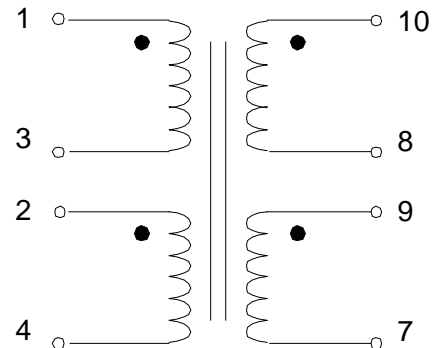


ADSL Line Interface Transformer

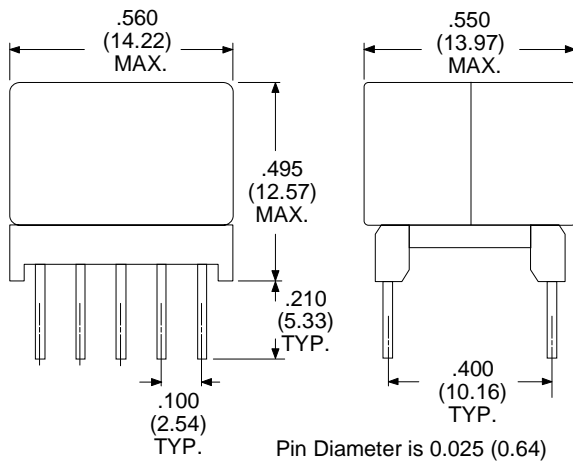
Designed for use with Analog Devices # AD20msp910/918

PARAMETER		UNITS
URNS RATIO	1 : 1 $\pm 2\%$	
Inductance ¹	5.0 $\pm 10\%$	mH
Inductance ²	5.0 $\pm 10\%$	mH
Leakage Inductance ³	18.0 Max.	μ H
DCR Pins 1 -4	3 Max.	Ω
DCR Pins 10 - 7	3 Max.	Ω
THD (Linearity) at 30 kHz	-80	dB
Longitudinal Balance at 25 kHz to 1.1 MHz	40 Min.	dB
Insertion Loss at 100 kHz	0.5 Min.	dB
Frequency Response at 30 kHz to 1.1 MHz	± 1.0 Max	dB
Isolation (1-4 to 7-10)	1500	V _{RMS}

Schematic Diagram

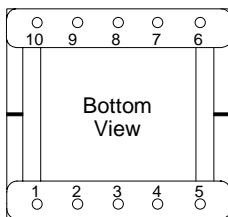
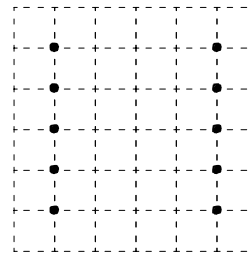


1. Tested @ 10 kHz and 100 mVAC (Pins 1-4) With 2-3 shorted.
2. Tested @ 10 kHz and 100 mVAC (Pins 10-7) With 8-9 shorted.
3. Tested @ 100 kHz and 100 mVAC (Pins 1-4) With 8-9, 2-3, 10-7 Shorted.



PIN POSITION GRID 0.100 (2.54)

● HOLES FOR BOBBIN PINS



Physical Dimensions
inches (mm)

Rhombus P/N: T-1308	
Cust. P/N:	Name:
Date: 8/23/99	Sheet: 1 of 1