

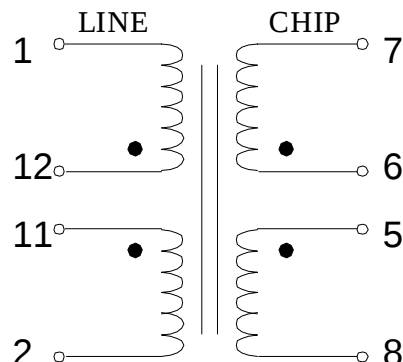
ADSL Line Transformer

Turns Ratio ($\pm 3\%$)

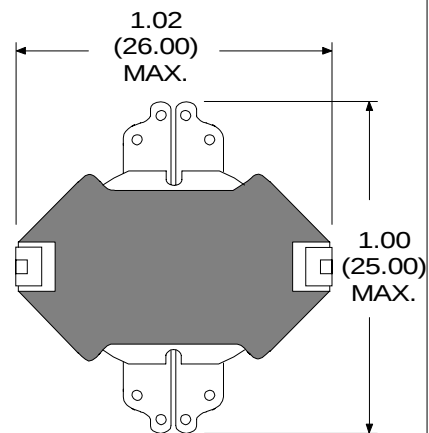
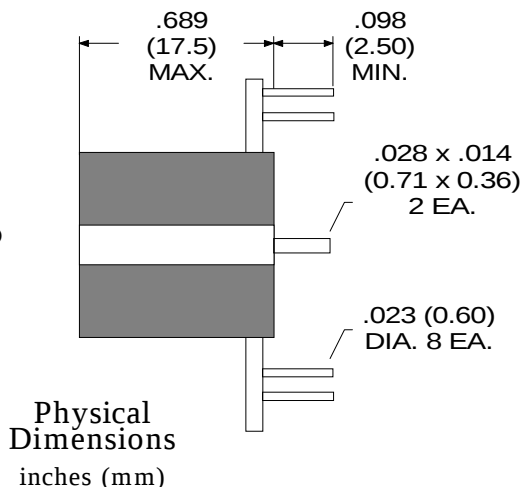
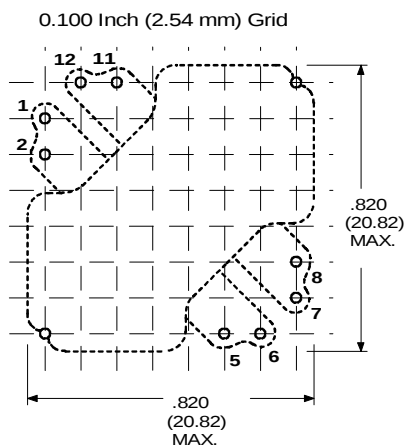
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PARAMETER	MIN.	MAX.	UNITS
Primary Inductance ⁽¹⁾ Pins 2 -12 (Pin 1-11 Short)	418.4	462.5	μH
Leakage Inductance ⁽²⁾ Pins 2-12 Short Pin 1-11 & Chip Side Shorted		10	μH
DC Resistance (Pin 12-1)		0.580	ohms
DC Resistance (Pin 11-2)		0.580	ohms
DC Resistance (Pin 6-7)		0.460	ohms
DC Resistance (Pin 5-8)		0460	ohms
Isolation (Hi-Pot) Chip to Line	2000		V_{RMS}
Isolation (Hi-Pot) Chip to Chip	500		V_{RMS}
Isolation (Hi-Pot) Line to Line	500		V_{RMS}
Isolation (Hi-Pot) Windings to Core	1500		V_{RMS}

Schematic Diagram



1. Tested at 10 KHz & 100 mV
2. Tested at 100 KHz & 100 mV



RHOMBUS P/N: T-1232

CUST P/N:

NAME:

DATE: 4/19/99

SHEET: 1 of 1