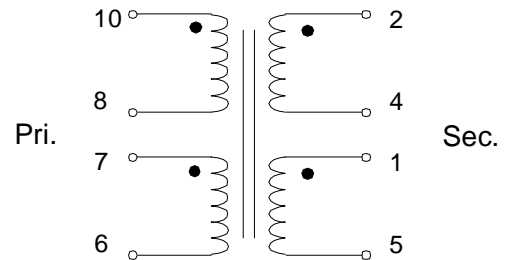


PARAMETER		UNITS
<b>URNS RATIO</b> Pins 10-6 : 2-5 short pins 8-7 : 4-1	1 : 2.12 ±1%	
Inductance <sup>1</sup>	2.25 ± 5%	mH
Leakage Inductance <sup>2</sup>	40 max.	μH
DCR Pins 2 - 4	1.8 ±15%	Ω
DCR Pins 1 - 5	1.8 ±15%	Ω
DCR Pins 10 - 8	0.42 ±15%	Ω
DCR Pins 7 - 6	0.64 ±15%	Ω
<b>Isolation</b> (Primary to Secondary)	2000	V <sub>RMS</sub>
DC Current	60 Max.	mA
<b>Long. Balance 2.5 to 300kHz</b> 2.5 to 300kHz	55 Min.	dB

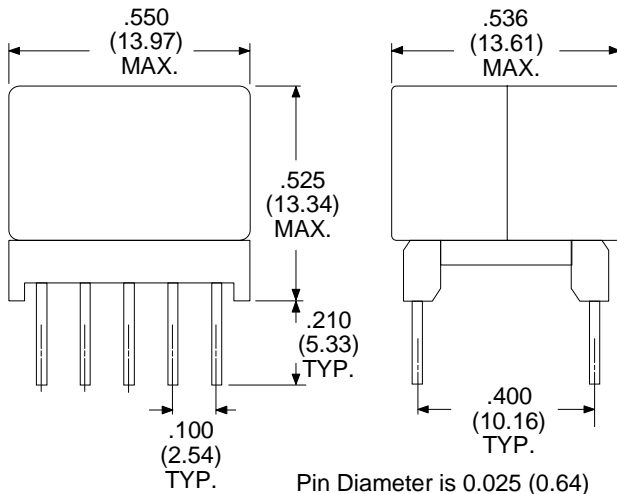
# HDSL Line Interface Transformer

Schematic Diagram



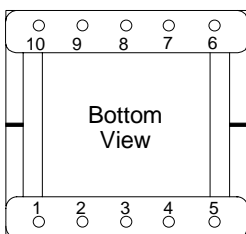
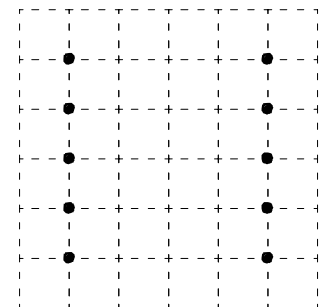
- 1) Tested Pins 2-5 @ 10 kHz and 0.1 V (Short Pins 4-1)  
 2) Tested Pins 2-5 @ 10 kHz 0.1 V (Short Pins 4-1 short 10-8, 7-6)  
 Tested on a Wayne Kerr 3245

## Physical Dimensions inches (mm)



PIN POSITION GRID 0.100 (2.54)

● HOLES FOR BOBBIN PINS



Rhombus P/N: <b>T-1225</b>	
Cust. P/N:	Name:
Date: <b>2/28/96</b>	Sheet: <b>1 of 1</b>