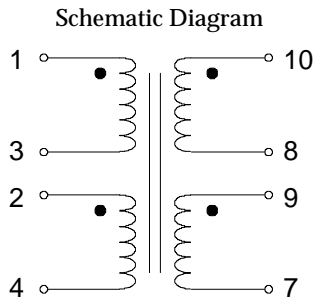
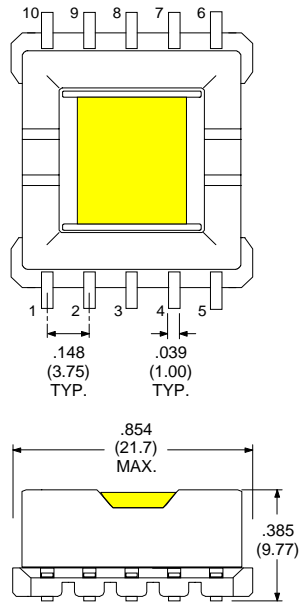


MSDSL Transformer Supplementary Insulation Requirements of IEC950

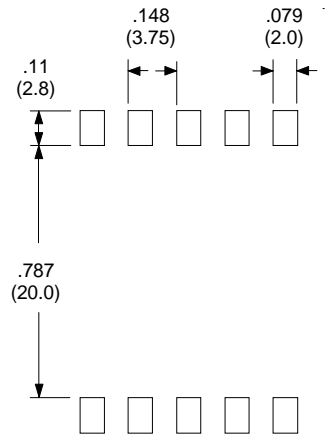


Turns Ratio (10-7) : (1-4) Shorted 2+3, 8+9	2 : 1 ±1%			
Turns Ratio (1-3) : (2-4)	1 : 1 ±1%			
Parameter	Min.	Typ.	Max.	Units
¹ Inductance 1-4 (shorted 2+3) Inductance 10-7 (shorted 8+9)	7.0 28		13 52	mH mH
² Leakage Inductance 1-4 Shorted 2+3, 8+9, 10+7		15		μH
Resistance pins (1-3)		0.46		ohms
Resistance pins (2-4)		0.46		ohms
Resistance pins (10-8)		1.10		ohms
Resistance pins (9-7)		1.10		ohms
Isolation (Hi-Pot) PRI - SEC	2400			V _{RMS}
Insertion Loss at 100k Per IEEE method, (22.1 ohm load)			0.20	dB
Longitudinal Balance 20 - 200kHz Per ITU method (L->M)	55		15	dB
THD, Total Harmonic Distortion: 81 dB typ., 10kHz, 3.53Vrms, 22.1 ohm load, 100 ohm input (shorted 2+3, 8+9).				

1. Tested at 10 KHz & 100 mV
2. Tested at 100 KHz & 100 mV



Physical Dimensions
inches (mm)



Suggested Solder Pad Layout

RHOMBUS P/N: T-1022G	
CUST P/N:	NAME:
DATE: 02/17/98	SHEET: 1 of 1